## B.Tech. 4th Semester F-Scheme (CSE) Examination, May-2018

## COMPUTER ARCHITECTURE AND ORGANIZATION

## Paper-CSE-210 F

Time	all	owed: 3 hours ] [ Maximum marks:	100			
Note		Question No. 1 is compulsory. Attempt f questions one question from each section.	live			
1.	Wr	rite short note on the following: 4×5	=20			
	(a)	MSAM				
	<b>(b)</b>	Master-slave flip-flop with its truth table				
	(c) Locality of Reference					
	(d)	Interrupts ersahilkagyan.com				
		Section-A				
2.	What do you mean by a multiplexer ? Describe a 4 * 1					
	mu	Itiplexer using AND, OR and NOR gate?	20			
3.	(a)	Explain Flynn's classification of computer	s in			
	2.72.072	detail.	10			
	(b)	Explain multilevel-viewpoint of a machine.	10			
		Section-B				
4.	(a)	How arithmetic Expressions are evaluated	in a			
	12,000	stack based CPU? Explain in detail.	10			

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	(b)	How a computer instruction is executed	i? Draw				
		the instruction cycle.	10				
5.	(a)	Explain various types of addressing mo	des with				
		example.	10				
	(b)	Explain CISC and RISC with example.	10				
		Section-C					
6.	Define Cache memory ? Explain following mapping						
	with	example: ersahilkagyan.com	20				
	(a)	Associative Mapping					
	(b)	Direct Mapping	67				
	(c)	Set-Associative Mapping.					
7.	(a)	(a) Differentiate between static and Dynamic Random					
		Access memory.	10				
	(b)	Write short note on 3-stage CPU cycle w	vith the				
	1.0	help of example.	10				
		Section-D					
8.	Wha	at is parallelism? Explain various typ	pes of				
	parallelism in detail.						
9.	(a)	State and Explain Amdahl's Law.	10				
	(b)	Write short note on memory hierarchy.	10				

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